

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants : Gosagan Padmanabhan et al.
Application No. : 10/779,855
Filed : February 17, 2004
For : METHOD FOR MANAGING A MICROPROCESSOR STACK FOR
SAVING CONTEXTUAL DATA

Examiner : Robert E. Fennema
Art Unit : 2183
Allowance
Date : April 25, 2008
Docket No. : 852663.405
Date : July 25, 2008

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

COMMENTS ON STATEMENT OF REASONS FOR ALLOWANCE

Commissioner for Patents:

In the Examiner's Reasons for Allowance with the Notice of Allowance mailed April 25, 2008, the Examiner stated that the limitation of "saving every register, except for a single register, when the contents of that single register equal a certain value" was not found in the prior art, but such identical language is not in any of the claims. It is understood that the Examiner was providing a summary of reasons for allowance. It is noted herein that to the extent that there are differences between the language used in the Statement and the language actually recited in each and every claim, it is the language recited in the claims that determines the scopes thereof. It is further noted herein that the present claims are allowable over all of the references on record. Accordingly, the scopes of the claims are not to be limited by the Statement.

Respectfully submitted,

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